

PatentCustomer No.: 31561
Application No.: 10/063,737
Docket No. 8727-US-PA**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Hsu
Application No. : 10/063,737
Filed : 2002/05/09
For : VOLTAGE REFERENCE CIRCUIT LAYOUT INSIDE
MULTI-LAYERED SUBSTRATE
Art Unit : 2825
Examiner : Bowers, Brandon**RECEIVED**
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002-1-703-872-9306

(Via fax: 14 pages, followed by confirmation copy via courier)

ASSISTANT COMMISSIONER FOR PATENTS
Arlington, VA22202

Dear Sirs,

In response to the Office Action dated June 10, 2004, please find the relevant paper in response to paper No. 20040606. Following the fax transmission, a hard copy via courier will also be forwarded to the Office.

Enclosed please find:

- ☒ Request for Continued Examination in (2) pages
- ☒ Preliminary Amendment in (9) pages
- ☒ Replacement Sheet of Drawing in (1) page
- ☒ Fax confirmation report
- ☒ Prepaid return postcard

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

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Patent

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Docket No. 8727-US-PA

Respectfully Submitted,
JIANQ CHYUN Intellectual Property Office

Date: Sept. 9, 2004

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